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PATENT
Attorney Docket No. 04329.3085

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Michio YAMASHITA et al.)	Group Art Unit: 2116
)	
Application No.: 10/607,218)	Examiner: Anand B. PATEL
)	
Filed: June 27, 2003)	
)	
For: CLOCK FREQUENCY CONTROL)	Confirmation No.: 5693
METHOD AND ELECTRONIC)	
APPARATUS)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(c)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), Applicants bring to the attention of the Examiner the documents on the attached listing. This Information Disclosure Statement ("IDS") is being filed after the events recited in Section 1.97(b) but, to the undersigned's knowledge, before the mailing date of either a Final action, Quayle action, or a Notice of Allowance. Under the provisions of 37 C.F.R. § 1.97(c), this Information Disclosure Statement includes a certification as specified by Section 1.97(e).

The documents listed in this IDS were first cited in a Communication and a minutes of telephone conversation regarding the Communication from the European Patent Office in a counterpart European Patent Application, and this IDS is filed within three months of mailing date of that communication.

Copies of both the Communication and the phone conversation summary are attached. Applicants do not endorse the conclusions set forth in the Communication and the phone conversation summary.

Applicants attach copies of the listed documents. Applicants respectfully request that the Examiner consider the listed documents and indicate that they were considered by making an appropriate notation on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed documents are material or constitute "prior art." If the Examiner applies any or all the documents as prior art against any claim in the application and Applicants determine that the cited documents do not constitute "prior art" under United States law, Applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of the documents.

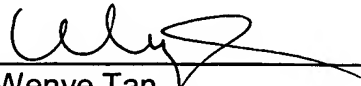
Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should the documents be applied against the claims of the present application.

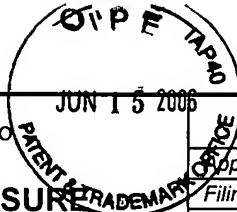
If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: June 15, 2006

By: 
Wenye Tan
Reg. No. 55,662



IDS Form PTO/SB/08: Substitute for form 1449A/PTO			Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Application Number	10/607,218	
			Filing Date	June 27, 2003	
			First Named Inventor	Michio YAMASHITA	
			Art Unit	2116	
			Examiner Name	Anand B. PATEL	
Sheet	1	of	2	Attorney Docket Number	04329.3085

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
Examiner Initials ¹	Cite No. ¹	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
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Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.

FOREIGN PATENT DOCUMENTS						
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶
		CHRISTOPHER J. HUGHES et al., Saving Energy with Architectural and Frequency Adaptations for Multimedia Applications, Conference Proceedings Article, December 1, 2001, pages 250-260, Proceedings. 34 th ACM/IEEE International Symposium December 1-5, 2001, Piscataway, NJ.	
		KINSHUK GOVIL et al., Comparing Algorithms for Dynamic Speed-Setting of a Low-Power CPU, available at http://citeseer.ist.psu.edu/govil95comparing.html .	
		Communication pursuant to Article 96(2) EPC mailed April 24, 2006, from European Patent Office in European Patent Application No. 03014280.6-2006.	
		Minutes of Telephone Conversation of April 28, 2006, from European Patent Office in European Patent Application No. 03014280.6-2006.	

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.